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July 30, 2003

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MAIL STOP PATENT APPLICATION

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Re.

Application of Hidemitsu MORI

SEMICONDUCTOR MEMORY DEVICE AND METHOD FOR

MANUFACTURING THE SAME

Assignee: NEC ELECTRONICS CORPORATION

Our Ref. Q76698



Attached hereto is the application identified above comprising 39 sheets of the specification, including the claims and abstract, 14 sheets of drawings, executed Assignment and PTO 1595 form, and executed Declaration and Power of Attorney. Also enclosed is an Information Disclosure Statement and PTO/SB/08 A & B (modified) with 1 reference.

The Government filing fee is calculated as follows:

Total claims Independent claims	$\frac{13}{3} - 20$	=	– x x	\$18.00 = \$84.00 =	
Base Fee			- '		\$750.00
TOTAL FILING FEE					\$750.00
Recordation of Assignment					\$40.00
TOTAL FEE					\$790.00

Checks for the statutory filing fee of \$750.00 and Assignment recordation fee of \$40.00 are attached. The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account. A duplicate copy of this transmittal letter is attached.





Mr. Mutsuji Asano July 30, 2003 Page 2

Priority is claimed from:

Country

Application No

Filing Date

Japan

2002-226151

August 2, 2002

The priority document is enclosed herewith.

Respectfully submitted, SUGHRUE MION, PLLC

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